

What is claimed is:

1. A circuit device comprising:

electrode terminals aligned in a plurality of columns along a first direction parallel to
5 an edge line of a semiconductor substrate and aligned in a plurality of rows along a second
direction perpendicular to the first direction; and

bumps disposed on the electrode terminal.

2. The circuit device of claim 1, wherein distances between adjacent two

10 electrode terminals aligned in the second direction are uniform.

3. The circuit device of claim 1, wherein each of the bumps has a rectangular
shape when viewed from an upper side of the bumps.

15 4. The circuit device of claim 3, wherein each of the bumps has at least one
chamfered corner portion.

5. The circuit device of claim 3, wherein each of the bumps has at least one
rounded corner portion.

20 6. The circuit device of claim 1, wherein each of the bumps has a pentagon, a
hexagon, an octagon or a circle shape when viewed from an upper side of the bumps.

7. A circuit device comprising:

25 electrode terminals aligned in a plurality of columns along a first direction parallel to
an edge line of a semiconductor substrate;

bumps disposed on the electrode terminals,

wherein the electrode terminals comprise first electrode terminals aligned in a first column and second electrode terminals aligned in a second column adjacent to the first column, distances between adjacent two first electrode terminals are uniform and the second electrode terminals are positioned at areas corresponding to areas between the first electrode terminals aligned in the first column.

8. The circuit device of claim 7, wherein each of the bumps has a predetermined length extended in a second direction perpendicular to the first direction, the bumps comprise first bumps disposed on the first electrode terminals and second bumps disposed on the second electrode terminals, the second bumps are separated from the first bumps and the separated distance between the first and second bumps is greater than a half of the predetermined length.

9. The circuit device of claim 8, wherein each of the bumps has a rectangular shape when viewed from an upper side of the bumps.

10. The circuit device of claim 9, wherein each of the bumps has at least one chamfered corner portion.

11. The circuit device of claim 9, wherein each of the bumps has at least one rounded corner portion.

12. A display device comprising:
a display panel having a display area on which a gate line, a data line perpendicular to the gate line and a plurality of pixels are formed and a peripheral area on which electrode

pads extended from the gate and data lines are formed, the peripheral area being disposed adjacent to the display area; and

a drive IC having electrode terminals aligned in a plurality of columns along a first direction parallel to an edge line of a semiconductor substrate and aligned in a plurality of rows along a second direction perpendicular to the first direction and bumps disposed on the electrode terminal, the drive IC being electrically connected to the electrode pads.

13. The display device of claim 12, wherein the electrode pads are positioned at the peripheral area, aligned in the plurality of columns along the first direction and aligned in the plurality of rows along the second direction perpendicular to the first direction.

14. The display device of claim 12, wherein each of the bumps has a rectangular shape when viewed from an upper side of the bumps.

15. The display device of claim 14, wherein each of the bumps has at least one chamfered corner portion.

16. The display device of claim 14, wherein each of the bumps has at least one rounded corner portion.

17. The display device of claim 12, wherein the drive IC is electrically connected to the display panel by means of an anisotropic conductive film.

18. The display device of claim 12, wherein the display panel is a liquid crystal display panel that displays an image by driving a liquid crystal.

19. A display device comprising:

a display panel having a display area on which a gate line, a data line perpendicular to the gate line and a plurality of pixels are formed and a peripheral area on which electrode pads extended from the gate and data lines are formed, the peripheral area being disposed adjacent to the display area; and

a drive IC having electrode terminals aligned in a plurality of columns along a first direction parallel to an edge line of a semiconductor substrate and bumps disposed on the electrode terminals,

wherein the electrode terminals comprise first electrode terminals aligned in a first column and second electrode terminals aligned in a second column adjacent to the first column, distances between adjacent two first electrode terminals are uniform and the second electrode terminals are positioned at areas corresponding to areas between the first electrode terminals aligned in the first column.

20. The display device of claim 19, wherein the electrode pads are positioned at the peripheral area and aligned in the plurality of columns along the first direction, the electrode pads comprise first electrode pads aligned in the first column and second electrode pads aligned in the second column, distances between adjacent two electrode pads are uniform, and the second electrode pads are positioned at areas corresponding to areas between the first electrode pads aligned in the first column.

21. The display device of claim 19, wherein each of the bumps has a predetermined length extended in a second direction perpendicular to the first direction, the bumps comprise first bumps disposed on the first electrode terminals and second bumps disposed on the second electrode terminals, the second bumps are separated from the first bumps and the separated distance between the first and second bumps is greater than a half

of the predetermined length.

22. The display device of claim 21, wherein each of the bumps has a rectangular shape when viewed from an upper side of the bumps.

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23. The display device of claim 22, wherein each of the bumps has at least one chamfered corner portion.

24. The display device of claim 22, wherein each of the bumps has at least one rounded corner portion.

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25. The display device of claim 19, wherein the drive IC is electrically connected to the display panel by means of an anisotropic conductive film.

26. The display device of claim 19, wherein the display panel is a liquid crystal display panel that displays an image by driving a liquid crystal.

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